

C1 cont'd
the substrate **10** and not embedded into the substrate **10**.

Please insert the following paragraph between lines 15 and 16 on page 18:

C2
In FIG. 8: the fourth circuit line **70** is shown as on the top surface **18** of the substrate **10** and not embedded into the substrate **10**; the fifth circuit line **72** is shown as on the bottom surface **19** of the substrate **10** and not embedded into the substrate **10**; and the sixth circuit line **74** is shown as on the bottom surface **19** of the substrate **10** and not embedded into the substrate **10**.

Please insert the following paragraph between lines 6 and 7 on page 24:

C3
In FIG. 10: the first circuit line **210** is shown as on the bottom surface **206** of the substrate **204** and not embedded into the substrate **204**; the second circuit line **220** is shown as on the bottom surface **206** of the substrate **204** and not embedded into the substrate **204**; and the third circuit line **230** is shown as on the bottom surface **206** of the substrate **204** and not embedded into the substrate **204**.

Please insert the following paragraph between lines 1 and 2 on page 26:

C4
In FIG. 11: the first circuit line **310** is shown as on the top surface **305** of the substrate **304** and not embedded into the substrate **304**; and the second circuit line **320** is shown as on the bottom surface **306** of the substrate **304** and not embedded into the substrate **304**.

Please insert the following paragraph between lines 7 and 8 on page 27:

C5
In FIG. 12: the first circuit line **410** is shown as on the top surface **405** of the substrate **404**